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J. Lee: N/E  
2/12/02  
BOX AFTER FINAL  
EXPEDITED PROCEDURE  
GROUP ART UNIT: 2818  
Docket No. 740819-408

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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE  
In re PATENT application of:  
Hiroaki NAKAOKA et al.  
Application Serial No. 09/640,519  
Filed: August 17, 2000  
For: METHOD OF FABRICATING  
SEMICONDUCTOR DEVICE  
Examiner: D. Vu  
Art Unit: 2818

**REQUEST FOR RECONSIDERATION**

**BOX AF**  
Commissioner for Patents  
Washington, D.C. 20231

Sir:

Responsive to the Office Action dated **September 26, 2001**, the period for response having been extended one (2) months until **February 26, 2002**, the following remarks are respectfully submitted in connection with the above-identified application.

**REMARKS**

Applicant would like to thank the Examiner for the telephone conference February 1, 2002, in which the Examiner agreed to withdraw the finality of the Office Action as a result of a lack of full consideration being afforded to claim 1.

The Office Action of **September 26, 2001** has been received and its contents carefully noted. Applicant submits that this response is timely filed and fully responsive to the Office Action. Claims 1-10 are currently pending in the present application and are believed to be in condition for allowance at least for the reasons advanced hereinbelow.

The Office Action rejects claims 1, 3 and 5-9 under 35 U.S.C. §102(b) as